Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	152	703/21.ccor.	US-PGPUB; USPAT	OR	ON	2004/12/10 20:11
S2	439	710/107.ccor.	US-PGPUB; USPAT	OR	ON	2004/12/10 19:31
S3	109	710/313.ccor.	US-PGPUB; USPAT	OR	ON	2004/12/10 19:33
S4	22658	object adj oriented	US-PGPUB; USPAT	OR	ON	2004/12/10 19:45
S5	747	S4 with abstract$3	US-PGPUB; USPAT	OR	ON	2004/12/10 19:46
S6	11873	(C++ java) with object	US-PGPUB; USPAT	OR	ON	2004/12/10 19:46
S7	396	S5 and S6	US-PGPUB; USPAT	OR	ON	2004/12/10 19:47
S8	49	root adj bus	US-PGPUB; USPAT	OR	ON	2004/12/10 19:48
S9	3	S5 and S8	US-PGPUB; USPAT	OR	ON	2004/12/10 19:51
S10	2	S7 and S8	US-PGPUB; USPAT	OR	ON	2004/12/10 19:52
S11	220	S7 and bus	US-PGPUB; USPAT	OR	ON	2004/12/10 19:52
S12	20	S11 and pci	US-PGPUB; USPAT	OR	ON	2004/12/10 19:53
S13	68	S11 and enumerat$4	US-PGPUB; USPAT	OR	ON	2004/12/10 19:54
S14	56	S13 and @ad<="20010308"	US-PGPUB; USPAT	OR	ON	2004/12/10 19:57
S15	13	S8 and enumerat$4	US-PGPUB; USPAT	OR	ON	2004/12/10 19:57
S16	7559	resource with configuration	US-PGPUB; USPAT	OR	ON	2004/12/10 19:56
S17	4291	S16 and bus	US-PGPUB; USPAT	OR	ON	2004/12/10 19:56
S18	613	S4 and S17	US-PGPUB; USPAT	OR	ON	2004/12/10 19:56
S19	155	S18 and enumerat$4	US-PGPUB; USPAT	OR	ON	2004/12/10 19:57
S20	94	S19 and @ad<="20010308"	US-PGPUB; USPAT	OR	ON	2004/12/10 20:03
S21	78	S20 and identifier	US-PGPUB; USPAT	OR	ON	2004/12/10 20:01
S22	27337	plug-in	US-PGPUB; USPAT	OR	ON	2004/12/10 20:01
S23	400	S22 with driver	US-PGPUB; USPAT	OR	ON	2004/12/10 20:04
S24	3	S5 and S23	US-PGPUB; USPAT	OR	ON	2004/12/10 20:02
S25	244	S23 and bus	US-PGPUB; USPAT	OR	ON	2004/12/10 20:03
S26	147	S25 and @ad<="20010308"	US-PGPUB; USPAT	OR	ON	2004/12/10 20:03
S27	2	S23 with chipset	US-PGPUB; USPAT	OR	ON	2004/12/10 20:05
S28	5	S5 with handle	US-PGPUB; USPAT	OR	ON	2004/12/10 20:05
S29	15678	bus same functional	US-PGPUB; USPAT	OR	ON	2004/12/10 20:11

S30	430	S29 same model$4	US-PGPUB; USPAT	OR	ON	2004/12/10 20:12
S31	185	S30 and resource	US-PGPUB; USPAT	OR	ON	2004/12/10 20:12
S32	156	S31 and configuration	US-PGPUB; USPAT	OR	ON	2004/12/10 20:12
S33	116	S32 and @ad<="20010308"	US-PGPUB; USPAT	OR	ON	2004/12/10 20:13
S34	59	S33 and pci	US-PGPUB; USPAT	OR	ON	2004/12/10 20:14
S35	3	S34 and identifier	US-PGPUB; USPAT	OR	ON	2004/12/10 20:13
S36	2	(("5974474") or ("6301011")).PN.	US-PGPUB; USPAT	OR	OFF	2004/12/10 20:14
S37	419	710/8.ccor.	US-PGPUB; USPAT	OR	ON	2004/12/12 19:27
S38	14	plug-in with chipset	US-PGPUB; USPAT	OR	ON	2004/12/12 19:42
S39	29	plug-in same chipset	US-PGPUB; USPAT	OR	ON	2004/12/12 19:44
S40	240	719/318.ccor.	US-PGPUB; USPAT	OR	ON	2004/12/12 19:45

PORTAL
US Patent & Trademark Office

Search: The ACM Digital Library The Guide
+"object oriented", +"bus representation"
SEARCH

THE ACM DIGITAL LIBRARY

Feedback Report a problem Satisfaction survey

Published before April 2001
Terms used object oriented bus representation

Found 3 of 111,600

Sort results by relevance
Display results condensed form

Save results to a Binder
Search Tips
Open results in a new window

Try an Advanced Search
Try this search in The ACM Guide

Results 1 - 3 of 3

Relevance scale

1 Improving performance of an electrical power expert system with genetic algorithms
Mike Goodloe, Sara Graves
June 1988
Proceedings of the first international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1
Full text available: pdf(1.57 MB) Additional Information: full citation, abstract, references, index terms

2 The Information Bus: an architecture for extensible distributed systems
Brian Oki, Manfred Pfluegl, Alex Siegel, Dale Skeen
December 1993
ACM SIGOPS Operating Systems Review , Proceedings of the fourteenth ACM symposium on Operating systems principles, Volume 27 Issue 5
Full text available: pdf(1.12 MB) Additional Information: full citation, abstract, references, citings, index terms

3 System design methodologies and experiences: Experiments with the peripheral virtual component interface
Roman L. Lysecky, Frank Vahid, Tony D. Givargis
September 2000
Proceedings of the 13th international symposium on System synthesis
Full text available: pdf(56.72 KB) Additional Information: full citation, abstract, references, citings

Results 1 - 3 of 3

Useful downloads: Adobe Acrobat QuickTime Windows Media Player Real Player

Published before April 2001
Terms used **object oriented pci bus**

Found 5 of 111,600

Sort results by relevance
Display results condensed form
Save results to a Binder
Search Tips
Open results in a new window

Try an Advanced Search
Try this search in The ACM Guide

Results 1 - 5 of 5

Relevance scale

1 Production information management for batch manufacturing plants based on ECA mechanism and view generation
Hideyuki Takada, Hiromitsu Shimakawa, Yoshitomo Asano, Morikazu Takegaki
November 1996 **Proceedings of the workshop on on Databases: active and real-time**
Full text available: pdf(418.02 KB) Additional Information: full citation, references, index terms

2 Java driven codesign and prototyping of networked embedded systems
Josef Fleischmann, Klaus Buchenrieder, Rainer Kress
June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**
Full text available: pdf(88.24 KB) Additional Information: full citation, references, citings, index terms

3 Embedding Linux to Control Accelerators and Experiments
A. Gotz, P. Makijarvi, B. Regad, M. Perez, P. Mangiagalli
October 1999 **Linux Journal**
Full text available: html(28.17 KB) Additional Information: full citation, abstract, references, index terms

4 The environment understanding interface: detecting and tracking human activity through multimedia sensors
Steven G. Goodridge
November 1995 **Proceedings of the 1995 conference of the Centre for Advanced Studies on Collaborative research**
Full text available: pdf(243.03 KB) Additional Information: full citation, abstract, references, index terms

5 System level design research in an industrial setting (invited talks): IP reuse in the system on a chip era
Warren Savage, John Chilton, Raul Camposano
September 2000 **Proceedings of the 13th international symposium on System synthesis**
Full text available: pdf(723.70 KB) Additional Information: full citation, abstract, references

Results 1 - 5 of 5

Full-text Search Prototype Results

Feedback Help

Your search matched **9** of **1043372** documents.
A maximum of **500** results are displayed, **50** to a page, sorted by **Publication year** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

object oriented<and>pci bus Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

---

1 **NPACI: rocks: tools and techniques for easily deploying manageable Linux clusters**
Papadopoulos, P.M.; Katz, M.J.; Bruno, G.;
Cluster Computing, 2001. Proceedings. 2001 IEEE International Conference on , 8-11 October 2001
Pages:258 - 267

[Abstract] [PDF Full-Text (168 KB)] IEEE CNF

---

2 **Using multirail networks in high-performance clusters**
Coll, S.; Frachtenberg, E.; Petrini, F.; Hoisie, A.; Gurvits, L.;
Cluster Computing, 2001. Proceedings. 2001 IEEE International Conference on , 8-11 October 2001
Pages:15 - 24

[Abstract] [PDF Full-Text (232 KB)] IEEE CNF

---

3 **Performance and architecture of SGSN and GGSN of general packet radio service (GPRS)**
Mishra, A.;
Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE , Volume: 6 , 25-29 Nov. 2001
Pages:3494 - 3498 vol.6

[Abstract] [PDF Full-Text (201 KB)] IEEE CNF

---

4 **System-level design: orthogonalization of concerns and platform-based design**
Keutzer, K.; Newton, A.R.; Rabaey, J.M.; Sangiovanni-Vincentelli, A.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 19 , Issue: 12 , Dec. 2000
Pages:1523 - 1543

[Abstract] [PDF Full-Text (452 KB)] IEEE JNL

---

5 **A Web-based TelePACS using an asymmetric satellite system**
Seon-Cheol Hwang; Myoung-Ho Lee;
Information Technology in Biomedicine, IEEE Transactions on , Volume: 4 , Issue: 3 , Sept. 2000
Pages:212 - 215

[Abstract] [PDF Full-Text (304 KB)] IEEE JNL

---

6 **MASAA: a case study in building a distributed integrated media database**

Zimmermann, R.; Kolahdouzan, M.R.; Shahabi, C.;
System Sciences, 2000. Proceedings of the 33rd Annual Hawaii International Conference on , 4-7 Jan. 2000
Pages:10 pp.

[Abstract] [PDF Full-Text (224 KB)] IEEE CNF

---

7 **Integrating communication protocol selection with hardware/software codesign**
Knudsen, P.V.; Madsen, J.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 18 , Issue: 8 , Aug. 1999
Pages:1077 - 1095

[Abstract] [PDF Full-Text (336 KB)] IEEE JNL

---

8 **A shared memory model on a cluster of PCs**
Houzet, D.;
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii International Conference on , Volume: Track3 , 5-8 Jan. 1999
Pages:10 pp.

[Abstract] [PDF Full-Text (84 KB)] IEEE CNF

---

9 **Hardware/software codesign for FPGA-based systems**
Saul, J.M.;
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii International Conference on , Volume: Track3 , 5-8 Jan. 1999
Pages:10 pp.

[Abstract] [PDF Full-Text (188 KB)] IEEE CNF

Full-text Search Prototype Results

Feedback Help

Your search matched **19** of **1043372** documents.
A maximum of **500** results are displayed, **50** to a page, sorted by **Publication year** in **Descending** order.

Refine This Search:
You may refine your search by editing the current search expression or entering a new one in the text box.

object oriented<and>bus<and>plug in<and>resource Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

---

1 **A layered modeling and simulation architecture for agent-based system development**
Sarjoughian, H.S.; Zeigler, B.P.; Hall, S.B.;
Proceedings of the IEEE , Volume: 89 , Issue: 2 , Feb 2001
Pages:201 - 213

[Abstract] [PDF Full-Text (332 KB)] IEEE JNL

---

2 **Reusable multimedia content in Web based learning systems**
El Saddik, A.; Fischer, S.; Steinmetz, R.;
Multimedia, IEEE , Volume: 8 , Issue: 3 , July-Sept. 2001
Pages:30 - 38

[Abstract] [PDF Full-Text (596 KB)] IEEE JNL

---

3 **A software architecture for open service gateways**
Gong, L.;
Internet Computing, IEEE , Volume: 5 , Issue: 1 , Jan.-Feb. 2001
Pages:64 - 70

[Abstract] [PDF Full-Text (196 KB)] IEEE JNL

---

4 **Adaptive collaboration for wired and wireless platforms**
Marsic, I.;
Internet Computing, IEEE , Volume: 5 , Issue: 4 , July-Aug. 2001
Pages:26 - 35

[Abstract] [PDF Full-Text (404 KB)] IEEE JNL

---

5 **Subject Index**

Computer , Volume: 34 , Issue: 12 , Dec. 2001
Pages:94 - 103

[Abstract] [PDF Full-Text (799 KB)] IEEE JNL

---

6 **An open platform for reconfigurable contr I**
Wills, L.; Kannan, S.; Sander, S.; Guler, M.; Heck, B.; Prasad, J.V.R.; Schrage, D.; Vachtsevanos, G.;

Control Systems Magazine, IEEE , Volume: 21 , Issue: 3 , June 2001
Pages:49 - 64

[Abstract] [PDF Full-Text (1056 KB)] IEEE JNL

---

7 **Continuous program optimization: Design and evaluation**
Kistler, T.; Franz, M.;
Computers, IEEE Transactions on , Volume: 50 , Issue: 6 , June 2001
Pages:549 - 566

[Abstract] [PDF Full-Text (3856 KB)] IEEE JNL

---

8 **A future application environment for BC Hydro's EMS**
Vaahedi, E.; Chang, A.Y.; Mokhtari, S.; Muller, N.; Irisarri, G.;
Power Systems, IEEE Transactions on , Volume: 16 , Issue: 1 , Feb 2001
Pages:9 - 14

[Abstract] [PDF Full-Text (72 KB)] IEEE JNL

---

9 **The JEDI event-based infrastructure and its application to the development of the OPSS WFMS**
Cugola, G.; Di Nitto, E.; Fuggetta, A.;
Software Engineering, IEEE Transactions on , Volume: 27 , Issue: 9 , Sept. 2001
Pages:827 - 850

[Abstract] [PDF Full-Text (1688 KB)] IEEE JNL

---

10 **A software architecture for integrative utility management system**
Xiaolu Li; Mingyan Gao; Jinsong Liu; Zhenhua Ding; Xianzhong Duan;
Power Engineering Society Winter Meeting, 2001. IEEE , Volume: 2 , 28 Jan.-1 Feb. 2001
Pages:476 - 480 vol.2

[Abstract] [PDF Full-Text (1028 KB)] IEEE CNF

---

11 **An industrial view of electronic design automation**
MacMillen, D.; Camposano, R.; Hill, D.; Williams, T.W.;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 19 , Issue: 12 , Dec. 2000
Pages:1428 - 1448

[Abstract] [PDF Full-Text (180 KB)] IEEE JNL

---

12 **Design lessons for building agile manufacturing systems**
Newman, W.S.; Podgurski, A.; Quinn, R.D.; Merat, F.L.; Branicky, M.S.; Barendt, N.A.; Causey, G.C.; Haaser, E.L.; Yoohwan Kim; Swaminathan, J.; Velasco, V.B., Jr.;
Robotics and Automation, IEEE Transactions on , Volume: 16 , Issue: 3 , June 2000
Pages:228 - 238

[Abstract] [PDF Full-Text (320 KB)] IEEE JNL

---

13 **Defining new markets for intelligent agents**
Amin, M.; Ballard, D.;
IT Professional , Volume: 2 , Issue: 4 , July-Aug. 2000
Pages:29 - 35

[Abstract] [PDF Full-Text (220 KB)] IEEE JNL

---

14 **Applying a 3-D-GUI to a distributed network management system**
Watanabe, N.; Igarashi, Y.; Hanaki, M.;
Selected Areas in Communications, IEEE Journal on , Volume: 18 , Issue: 5 , May 2000
Pages:715 - 722

[Abstract] [PDF Full-Text (352 KB)] IEEE JNL

---

15 **An interactive learning environment for VLSI design**

Allen, J.; Terman, C.J.;
Proceedings of the IEEE , Volume: 88 , Issue: 1 , Jan. 2000
Pages:96 - 106

[Abstract] [PDF Full-Text (1356 KB)] IEEE JNL

---

16 **Web development: estimating quick-to-market software**
Reifer, D.J.;
Software, IEEE , Volume: 17 , Issue: 6 , Nov.-Dec. 2000
Pages:57 - 64

[Abstract] [PDF Full-Text (960 KB)] IEEE JNL

---

17 **A framework for building intelligent manufacturing systems**
Devedzic, V.; Radovic, D.;
Systems, Man and Cybernetics, Part C, IEEE Transactions on , Volume: 29 , Issue: 3 , Aug. 1999
Pages:422 - 439

[Abstract] [PDF Full-Text (316 KB)] IEEE JNL

---

18 **Synchronisation primitives for highly parallel discrete event simulations**
Kerridge, J.; Welch, P.; Wood, D.;
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii International Conference on , Volume: Track8 , 5-8 Jan. 1999
Pages:10 pp.

[Abstract] [PDF Full-Text (96 KB)] IEEE CNF

---

19 **Integrative systems: assessing requirements and capabilities for intra- and inter-organizational contexts**
Sutherland, J.W.;
Systems, Man and Cybernetics, Part A, IEEE Transactions on , Volume: 28 , Issue: 2 , March 1998
Pages:159 - 182

[Abstract] [PDF Full-Text (368 KB)] IEEE JNL

CiteSeer Find: represent and pci bus and object Documents Citations

Searching for **represent and pci bus and object**.
Restrict to: Header Title Order by: Expected citations Hubs Usage Date Try: Google (CiteSeer) Google (Web) Yahoo! MSN CSB DBLP
2 documents found. **Order: number of citations.**

The Ispd98 Circuit Benchmark Suite - Alpert (1998) (Correct) (43 citations)
circuits are now obsolete, and do not adequately **represent** the complexity of modern designs. Consequently, bus arbitrators, bus bridge chips, memory and **PCI bus** interfaces, communication adaptors, memory improvement" on a design with 20 thousand moveable **objects** is not nearly as interesting or relevant as a
vlsicad.cs.ucla.edu/~cheese/papers/ispd98.ps

One or more of the query terms is very common - only partial results have been returned. Try Google (CiteSeer).

Smart Interfaces + Safe Mechanisms = Human Friendly.. - Heinzmann, Matsumoto.. (1998) (Correct)
As a result of model fitting, six parameters to **represent** the position and orientation of the face are video stream. The hardware consists of a single **PCI-bus** card, running on an Pentium processor running human world i.e. won't be able to lift or carry **objects** of any significance. The only alternative is to
wwwsyseng.anu.edu.au/rsl/hfr98.ps

Try your query at: Google (CiteSeer) Google (Web) Yahoo! MSN CSB DBLP

CiteSeer Find: pci bus and object oriented Documents Citations

Searching for **pci bus and object oriented**.
Restrict to: Header Title Order by: Expected citations Hubs Usage Date Try: Google (CiteSeer) Google (Web) Yahoo! MSN CSB DBLP
12 documents found. **Order: number of citations.**

PAM-Blox: High Performance FPGA Design for Adaptive Computing - Mencer, Morf, Flynn (1998) (Correct) (13 citations)
Interface Pci Lca3 Lca1 Lca0 Lca2 Pif E E E **Pci Bus** Host Xc4000 Xc4000e Xc4000 Xc4000 Xc4000e Figure
set by the clock speed and bus-width of the **PCI bus**. PCI Pamette supports 32 and 64 bit PCI at 33 and 66
Abstract PAM-Blox are **object-oriented** circuit generators on top of the PCI Pamette
arith.stanford.edu/oskar/fccm98.ps

A Scalable Real-time Signal Processor for Object-oriented.. - Scherrer, Eberle (1998) (Correct) (1 citation)
structures based on busses. Examples are the **PCI bus** and Ethernet network. However, busses have
1 A Scalable Real-time Signal Processor for **Object-oriented** Data Flow Applications Daniel Scherrer, Hans
www.switcherland.ethz.ch/papers/pdcs98.ps

Unknown - (Correct)
if a standard bus controller (e.g. IEEE 488-**PCI bus** interfaces) is chosen, an additional search is
A similar goal can also be reached with **object oriented** languages, like Java and Cwithout
eprints.biblio.unitn.it/archive/00000608/01/Management_of_Distributed_Measurement_Systems_based_on_Client-Server_Paradigms.pdf

IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO.. - Modeling Benjamin Bishop (1993) (Correct)
be connected to a Linux host system through the **PCI bus**. The bus interface will be implemented as an
Video Technology, Vol. Xx, No. Y, Month 20zz **Object-Oriented** Bounding Boxes. Lin Et Al. 8] Give A Good
www.cs.scranton.edu/~bishop/TCSVT.ps

Zero-Copy for CORBA --- - Efficient Communication For (Correct)
Part Of The System. User Space Kernel Space Nic **Pci Bus** Application Application Mpi Mpi Socket Api
in CORBA we preserve the advantages of **object oriented** abstraction for the software design process
www.cs.inf.ethz.ch/cops/publications/../hpdc12/hpdc12.ps.gz

A Powerful System Design Methodology Combining OCAPI and .. - Concept Engineering.. (2002) (Correct)
between processor and FPGA is established via the **PCI bus** by shared memory DMA transfers. This paper
systems-on-chip. OCAPI's design methodology is **object-oriented** and design issues are solved with rich C
www.sigda.org/Archives/ProceedingArchives/Date/Date2002/papers/2002/date02/htmfiles/sun_sgi/../../pdffiles/08e_3.pdf

Object-Oriented High-Level Modeling of an InfiniBand to PCI-X.. - Lachish, Ziv (Correct)
between the InfiniBand network and the **PCI bus**. We could easily simulate many scenarios
Object-Oriented High-Level Modeling of an InfiniBand to
www.research.ibm.com/pics/verification/ps/hlm_fdl02.pdf

Formal Specification of the Virtual Component Interface.. - Bunker, Gopalakrishnan (2001) (Correct)
Section 4 briefly describes our previous VCI-to-**PCI bus** wrapper verification case study and the lessons
operational semantics for UML Use Cases in an **object-oriented** specification language named Odal. Again,
www.cs.utah.edu/~abunker/pubs/uucs-01-007.ps

Embedded Programming with C++ - Stephen Williams Picture (Correct)
support package a console driver that uses the **PCI bus** to communicate as a console. The MON960 monitor
Proceedings of the Third USENIX Conference on **Object-Oriented** Technologies and Systems Portland, Oregon,
www.sage.usenix.org/publications/library/proceedings/coots97/full_papers/williams/williams.ps

Collaborative Efforts To Support System-Level Design Education - Bouldin (Correct)
ARM, MIPS and Sparc#MPEG decompression engines, **PCI bus** controllers, specialized DSPs, etc. Combining
#Support for heterogeneity through **object-oriented** encapsulation, Emphasis on veri#cation
microsys6.engr.utk.edu/ece/collab99.pdf

A programming environment to control switching.. - Legrand.. (1996) (Correct)
transparently. Interfacing this chip with a **PCI-bus** by using fast FIFOs for matching the speed
in Java(tm) Sun Microsystems) Java is an **object oriented** language which is platform independent
www.ifh.de/~legrand/Papers/aihenp96.ps.gz

The C// Data Parallel Language on a Shared Memory Multiprocessor - Fatni, Houzet (Correct)
efficient communication mechanism. Features in the **PCI bus** and the network module are combined to provide a
as a shared-memory machine. Many parallel **object oriented** implementations based on C/Chave been
www.enseeiht.fr/Recherche/Info/Archit/CAMPB97.ps